

Figure 1

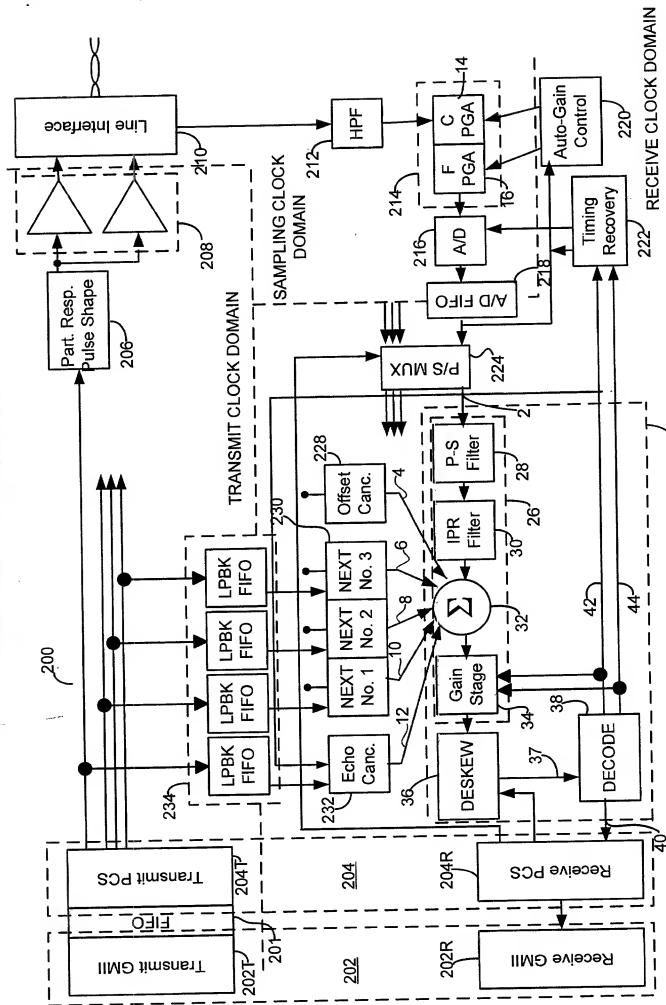
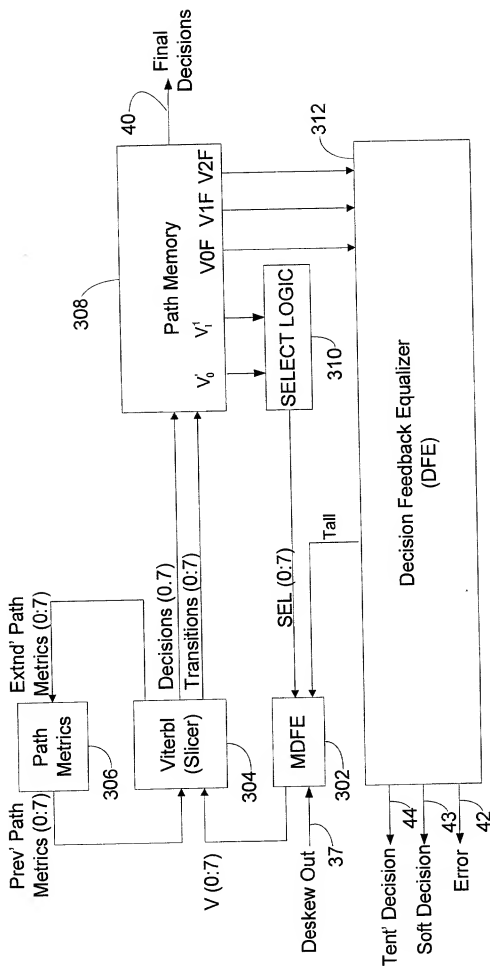


Figure 2



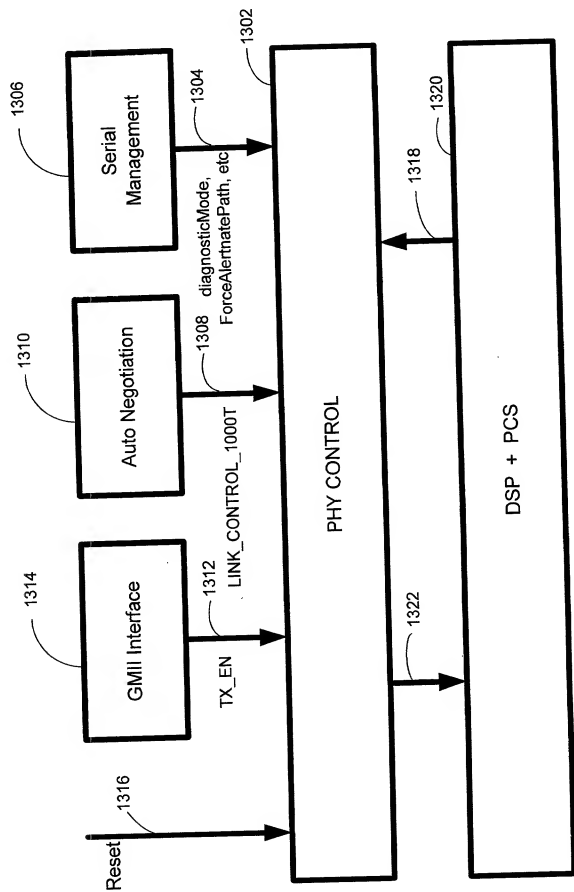


FIG. 4

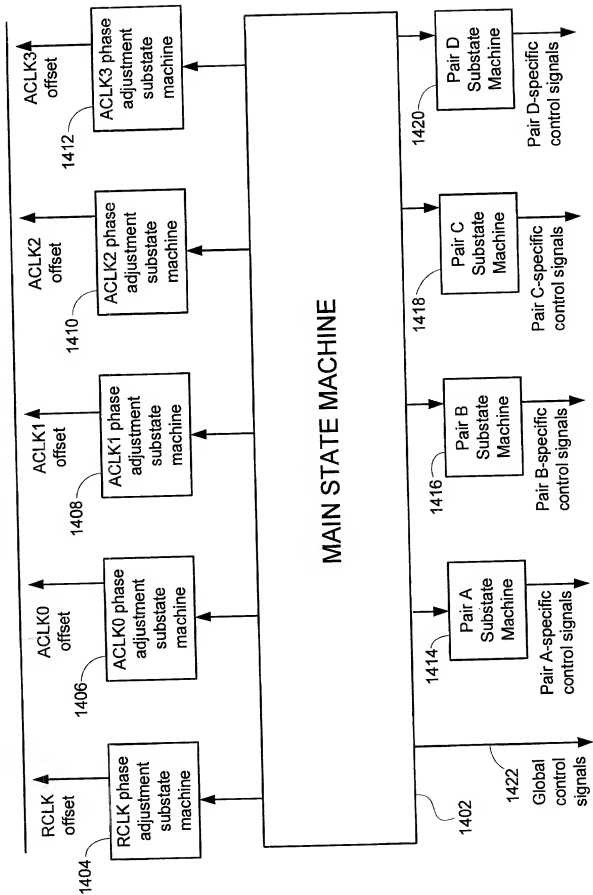


Figure 5

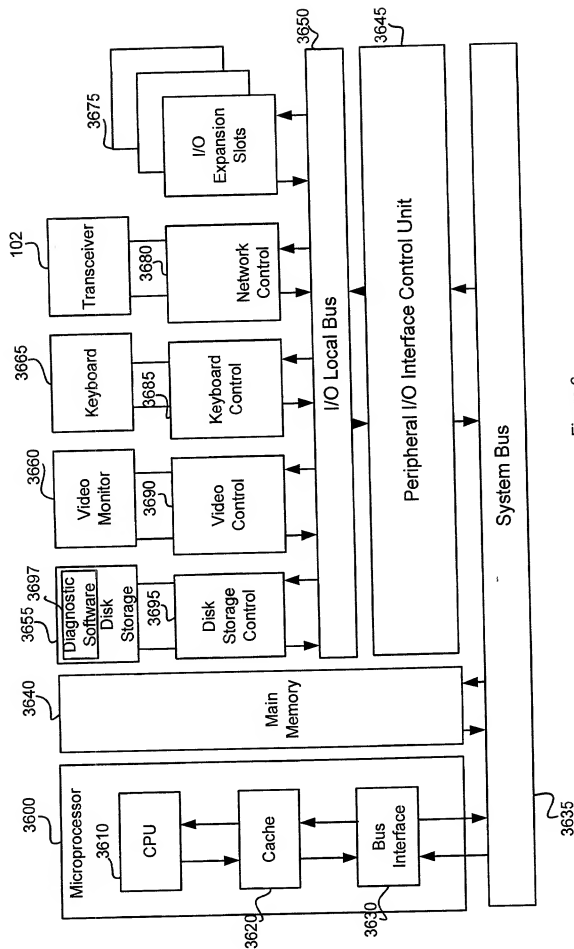


Figure 6

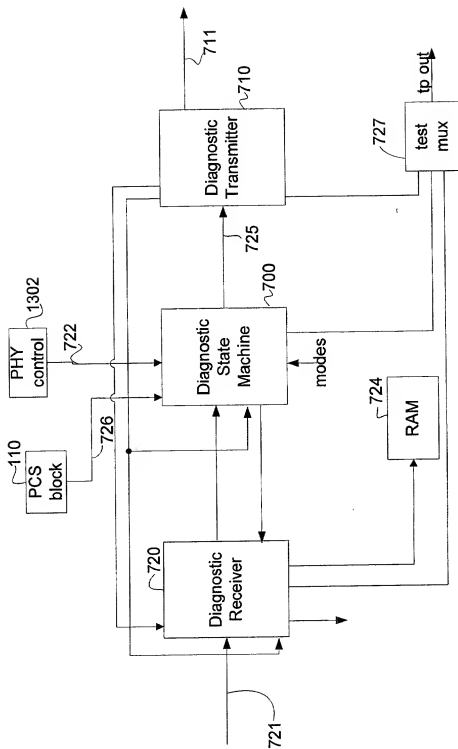


Figure 7

Master - no FEXT

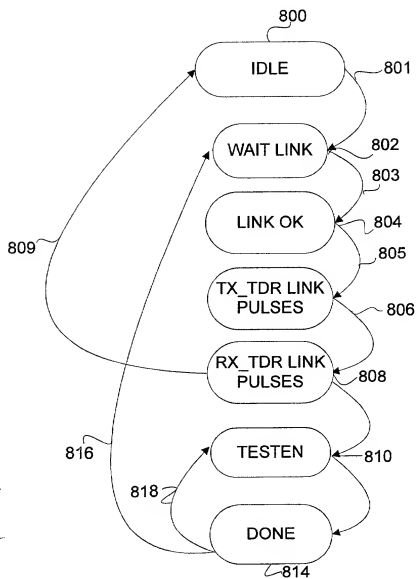


Figure 8



slave - no FEXT

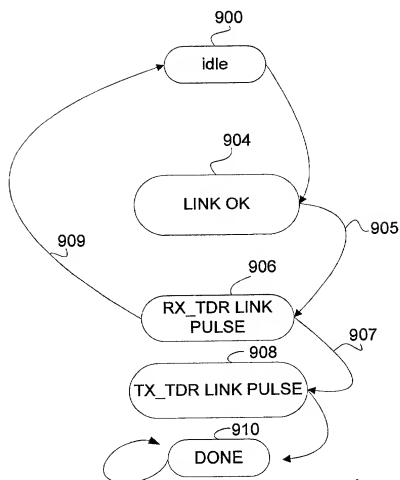


Figure 9

Echo/Next - no extphy

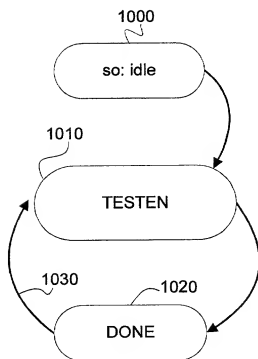


Figure 10

10086725.030402

Master - FEXT

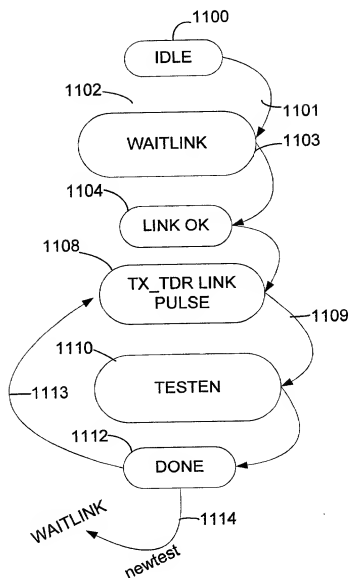


Figure 11



linkdet fsm

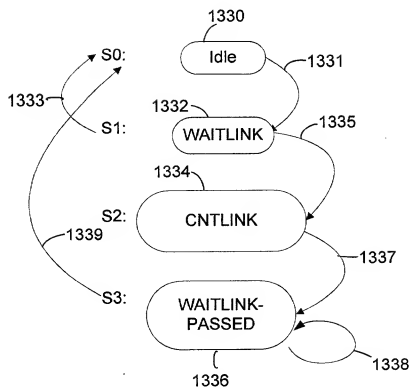


Figure 13

204020'5279800T

# FEXT: and signaling

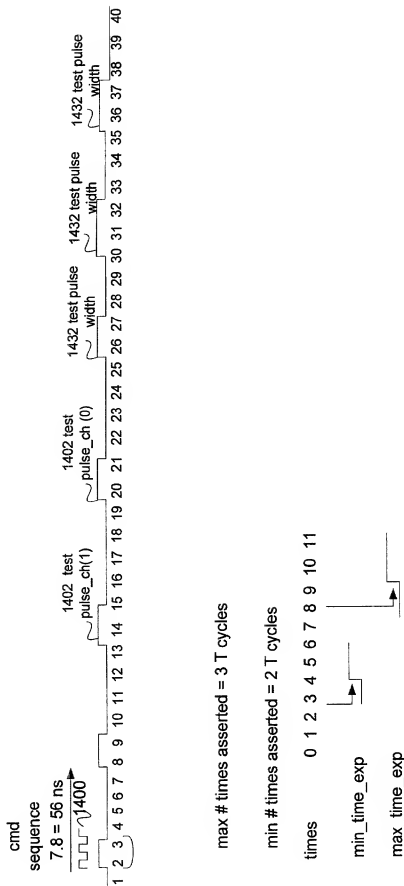


Figure 14

# LINKPULSE/TEST PULSE TIMING - NO FEXT

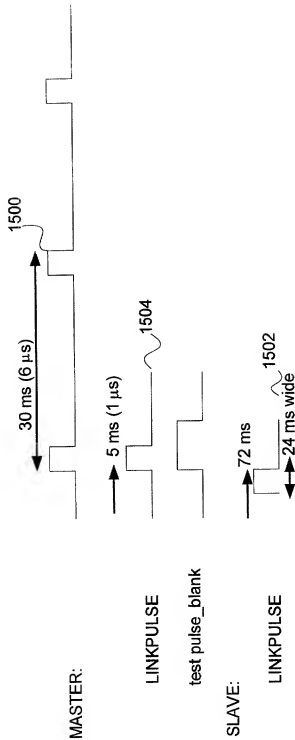


Figure 15

MDIX: Link pulse - no fext

1) No mdix

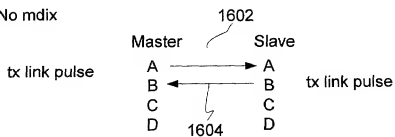


Figure 16A

2) mdix

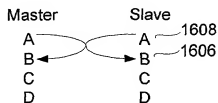


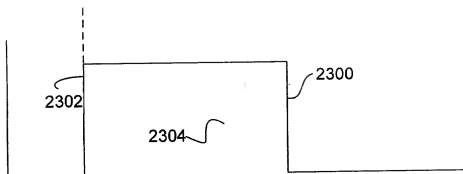
Figure 16B

10086725-030402



20060522/98007 10086725.030402

Test  
Signal



Data  
Signal

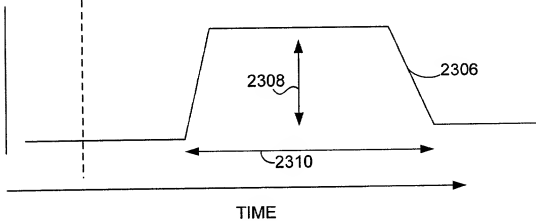


Figure 17

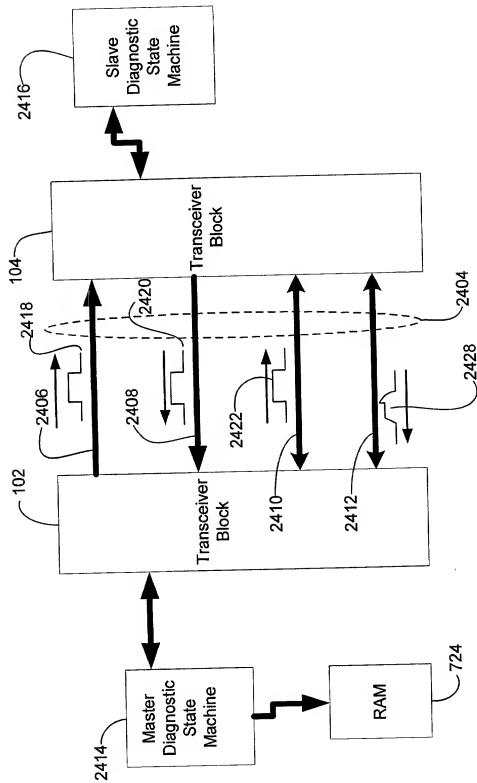


Figure 18

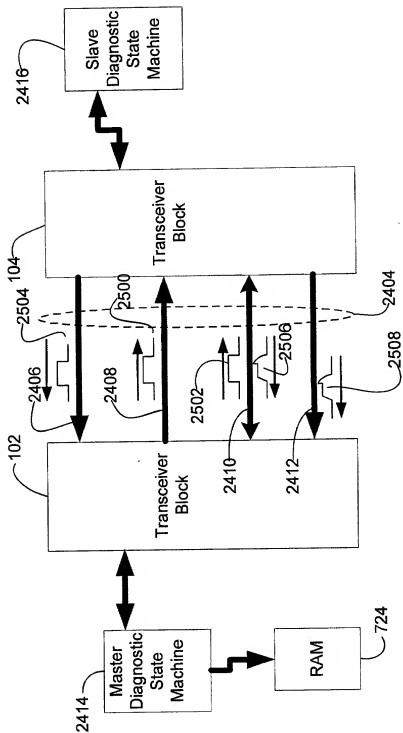


Figure 19

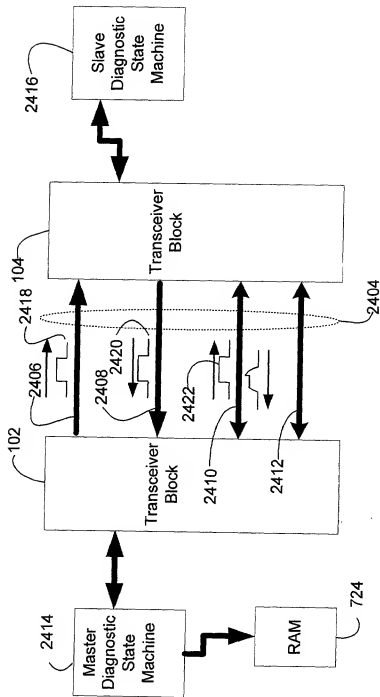


Figure 20